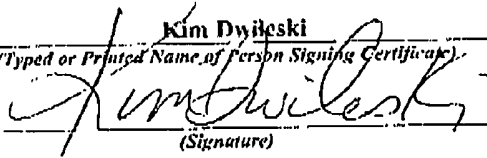


<b>CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)</b>			Docket No. <b>BUR920010100US1</b>
Applicant(s): <b>Joseph A. Iadanza</b>			
Serial No. <b>09/683,872</b>	Filing Date <b>2/26/2002</b>	Examiner <b>Ortiz, Edgardo</b>	Group Art Unit <b>2815</b>
Invention: <b>METHOD OF CONNECTING CORE I/O PINS TO BACKSIDE CHIP I/O PADS</b>			
			<b>RECEIVED CENTRAL FAX CENTER</b>
			<b>MAR 24 2004</b>
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I hereby certify that this <u>Amendment (13 pages)</u> (Identify type of correspondence)			
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<div style="text-align: center;"><u>Kim Dwileski</u> (Typed or Printed Name of Person Signing Certificate)  (Signature)</div>			
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**Docket No. BUR920010100US1**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant: Joseph A. Iadanza**

**Group Art Unit: 2815**

**Filed: 2/26/2002**

**Examiner: Ortiz, Edgardo**

**Serial No.: 09/683,872**

**Title: METHOD OF CONNECTING CORE I/O PINS TO BACKSIDE CHIP I/O PADS**

**Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**

**AMENDMENT**

**Sir:**

**In response to the Office Action mailed January 14, 2004, please amend the above-identified application as follows:**

**09/683,872**

**1**

**OFFICIAL**